

FEB 11 U.S. PTO  
10078292

03/15/02

U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10078292	02/15/2002	702		2857	Scout

\*\*APPLICANTS: Mimberg Ludger; Wagner Barry; Lao Mau;

\*\*CONTINUING DATA VERIFIED: None

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\*\* FOREIGN APPLICATIONS VERIFIED: None

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	NVID-P000406
Verified and Acknowledged Examiners initials X S			
TITLE : Method and system for dynamic power supply voltage adjustment for a semiconductor integrated circuit device			
U.S. DEPT. OF COMM./PAT. & TM. PTO-435L (Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
Amount Due	Date Paid		Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
TERMINAL DISCLAIMER			Sheets Drwg.	Figs.Drwg.
		Application Examiner		
PREPARED FOR ISSUE				
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